

Notice of References Cited	Application/Control No. 09/928,681	Applicant(s)/Patent Under Reexamination NATORI, TAKEHISA	
	Examiner Peter J Macchiarolo	Art Unit 2879	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
	B	US-			
	C	US-			
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FOREIGN PATENT DOCUMENTS

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Shimizu, "Formation of Display Transistor Array Panel," JP 11-142878, machine translation from JPO website.
	V	
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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